# The agony of choice - the diversity of microkernels in Genode



Stefan Kalkowski

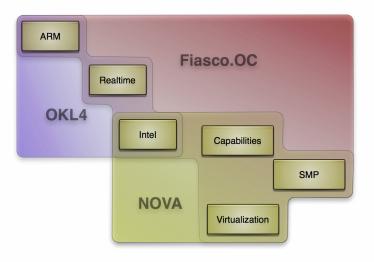


#### Outline

- 1. Advantages of diversity
  - Motivation
  - Code quality
  - Handling the multeity
- 2. Porting Genode to a kernel
  - What is needed
  - Course of action
  - Conclusion



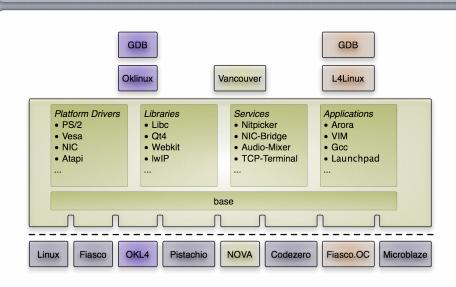
# Multi-Dimensional Feature Space





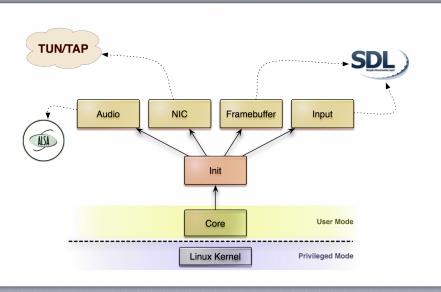


#### Get All-Inclusive



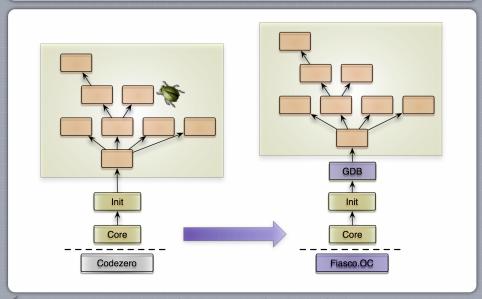


## Convenient Developing Under Linux



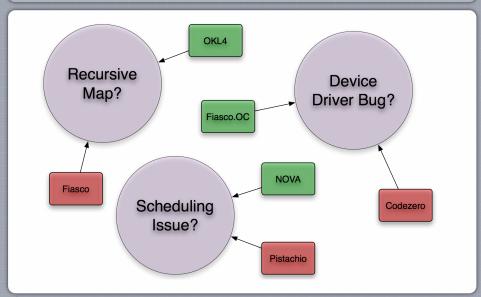


# Develop For One, Debug With Another





## Problem Localization





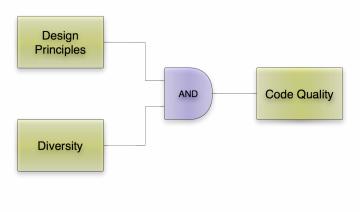
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## **Enhanced Code Quality**





Low complexity





- Low complexity
- Strive for a narrow API



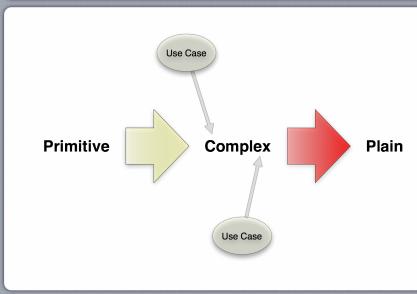
- Low complexity
- Strive for a narrow API
- Unify wherever possible, avoid code duplication



- Low complexity
- Strive for a narrow API
- Unify wherever possible, avoid code duplication
- No premature optimization

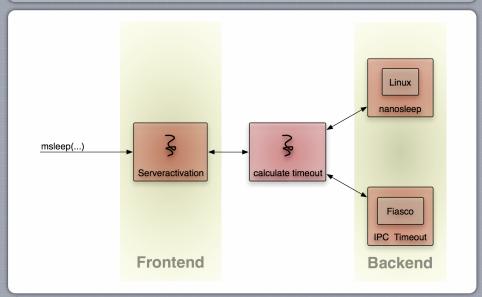


## Component's Lifecycle





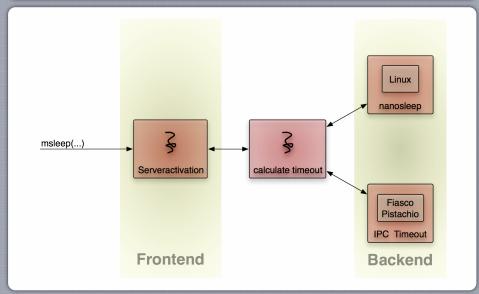
## Timer - An Unfinished Example





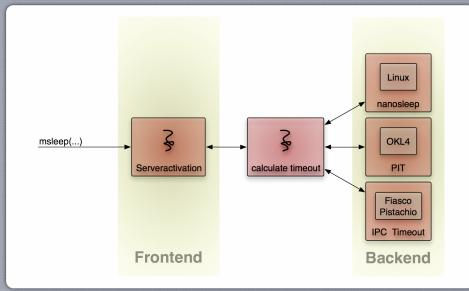


## Timer - Introducing Pistachio





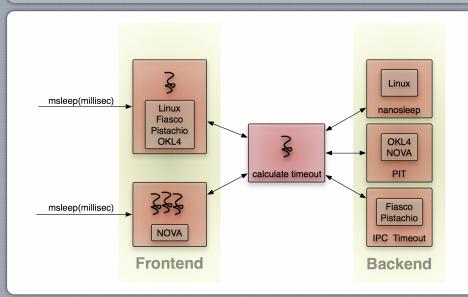
## Timer - Introducing OKL4







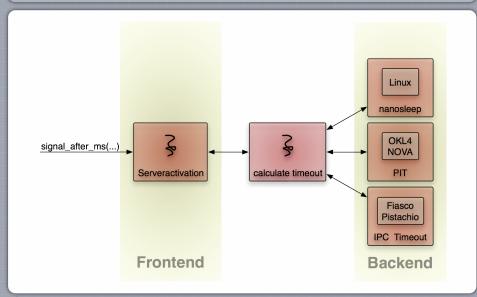
## Timer - Introducing NOVA







## Timer - Change API





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Plethora of tools needed





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- Knowledge of build-systems required



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- How to boot the system



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- Unmaintained software



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Solution: unify toolchain + convenience tools





## Demo

Short demo ...



## Run-Scripts

```
# Example run-script
build {
    core init drivers/timer ....
create_boot_directory
install config { ... }
set boot modules {
    core init timer ...
lappend_if [have_spec linux] boot_modules fb_sdl
build_boot_image $boot_modules
append qemu args " -m 256 "
run genode until forever
```

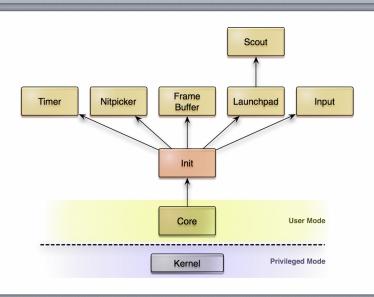


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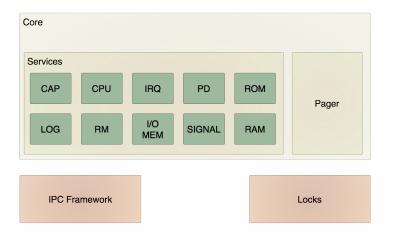


## Goal





## Kernel-Specific Parts





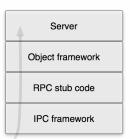
## **IPC Framework**

Client

Connection

RPC stub code

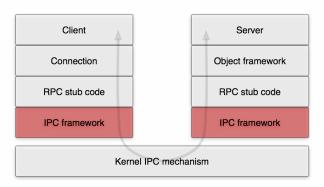
IPC framework



Kernel IPC mechanism

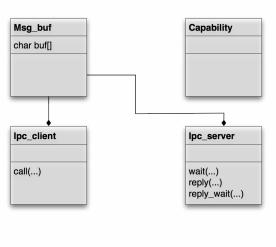


## **IPC Framework**



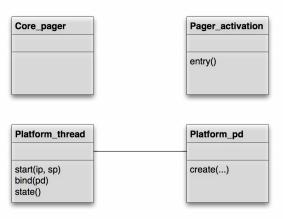


## **IPC Framework**





# Threads, Protection Domains, Pager





#### Lock

```
* base-<kernel>/src/base/lock helper.h
void thread_yield();
bool thread_check_stopped_and_restart(Native_thread_id id);
Native_thread_id thread_get_my_native_id();
Native_thread_id thread_invalid_id();
bool thread_id_valid(Native_thread_id id);
void thread_switch_to(Native_thread_id id);
void thread_stop_myself();
```



#### Platform Information

- Parse kernel + bootloader info
- Platform specific compile-time knowledge



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- Parse kernel + bootloader info
- Platform specific compile-time knowledge
- Sizing allocators and databases for
  - ► RAM
  - ▶ ROM modules
  - ► IRQ numbers
  - ► I/O memory (and ports)

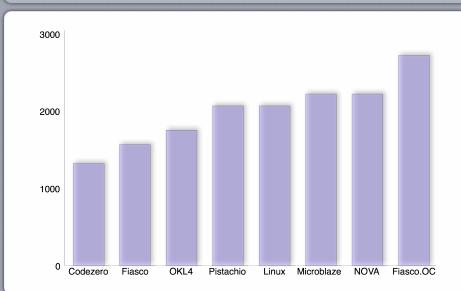


# Things Left

- Interrupts
- Timer
- Signals



## Effort: Kernel-Specific LOC





## What Benefit Do I Have?





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